

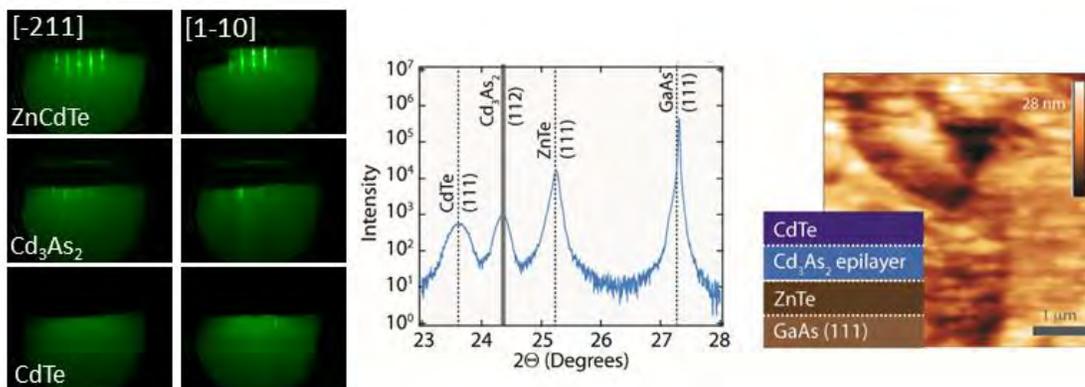
# MBE growth of $\text{Zn}_x\text{Cd}_{1-x}\text{Te}$ on $\text{Cd}_3\text{As}_2$

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The Dirac semimetal  $\text{Cd}_3\text{As}_2$  has become a scientifically useful material, as it provides access to a variety of interesting phenomena ranging from topological superconductivity to massless Dirac fermions. It is also potentially useful for energy-related applications due to its high electron mobility and large phonon-phonon scattering. While thin film growth has become an increasingly popular route of synthesis, there have been no reports of epitaxial growth on  $\text{Cd}_3\text{As}_2$ , limiting the ability to develop full heterostructures. One barrier to epitaxy on  $\text{Cd}_3\text{As}_2$  is the high vapor pressure of  $\text{Cd}_3\text{As}_2$  ( $1\text{e-}7$  mbar at only  $135^\circ\text{C}$ ), which is well below the ideal growth temperature of most semiconductors [1].

The  $\text{Zn}_x\text{Cd}_{1-x}\text{Te}$  (111) system provides one promising option for overgrowth given that it can be lattice matched to the (112) surface of  $\text{Cd}_3\text{As}_2$  and has a relatively low optimal growth temperature ( $240\text{-}300^\circ\text{C}$ ). Reflective high-energy electron diffraction (RHEED) patterns of epilayers grown on  $\text{Cd}_3\text{As}_2$  exhibit a (2x1) reconstruction, even with a growth interruption. These patterns are consistent with As-terminated CdTe observed following high temperature annealing under As [2]. CdTe epilayers growth at  $\text{Cd}_3\text{As}_2$ -compatible substrate temperatures ( $\sim 120^\circ\text{C}$ ) are rough, but further growth at  $240^\circ\text{C}$  yields smoother surfaces, as seen in atomic force microscopy. X-ray diffraction confirms  $\text{Cd}_3\text{As}_2$  remains following the higher temperature growth step, suggesting complete coverage is achieved. Higher temperature anneals under As further smooth and passivate this surface, while similar anneals under Te result in disappearance of a RHEED pattern and loss of the  $\text{Cd}_3\text{As}_2$  layer. Introduction of moderate Zn content into CdTe results in complete surface coverage but also very large features, likely a result of very low Zn adatom mobility at these temperatures. Our results provide a starting point for incorporating  $\text{Cd}_3\text{As}_2$  into a variety of device structures.



**Figure 1.** RHEED, XRD, and AFM of  $\text{CdTe}/\text{Cd}_3\text{As}_2$  structures

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