## Governing metal-insulator transition in ultra-thin VO<sub>2</sub> films by surface engineering



## SUPPLEMENTAL

**Figure 1.** Morphology and transport measurements of the 15 nm thick VO<sub>2</sub> films grown as-deposited and with a 1.5 nm vanadium oxide buffer layer (VO<sub>x</sub> buffered) on r-cut sapphire substrates. a, b) 1.0  $\mu$ m<sup>2</sup> scan AFM height image of the buffer layer and after 15 nm VO<sub>2</sub> film grown on the buffer layer, respectively. c) XRR measurements for as-deposited and VO<sub>x</sub>-buffered 15 nm samples. d) Resistance vs temperature for corresponding as-deposited 15 nm thick VO<sub>2</sub> (blue, round symbols) and with a 1.5 nm vanadium oxide buffer layer (green, triangles).

**Funding Acknowledgement:** This work was supported as part of the Quantum Materials for Energy Efficient Neuromorphic Computing (Q-MEEN-C), an Energy Frontier Research Center funded by the U.S. Department of Energy, Office of Science, Basic Energy Sciences under Award # DE-SC0019273