

Controlled and Selective etches for Gate All-Around Device Fabrication

Subhadeep Kal, Jeffery Smith, Nihar Mohanty, Yongzhuo Su, Cheryl Pereira, Aelan Mosden, Peter Biolsi, Trace Q. Hurd.

TEL Technology Center, America, LLC, 255 Fuller Rd., STE 214, Albany, NY, USA 12203

Supplementary information

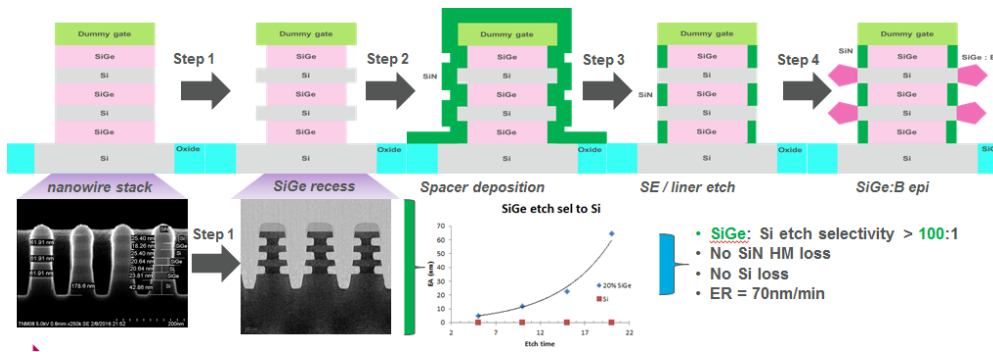


Figure1. Schematic patterning integration flow for a Si NW based GAA device architecture; with gas phase SiGe etch process performance and selectivity data.